BEng (Hons) Electronic and Communication Engineering - E431
(Under Review)

1. Introduction

The field of Electronic and Communication Engineering is one that currently evolves at an extremely fast rate. The market needs for better, faster and more efficient technologies provide the impetus necessary to push the boundaries of electronic and communication engineering. New and innovative products are released on the market every day and technologies developed in the field today become redundant and outdated within two to three years.

Given the nature of the field, the major challenge for an educational institution is to keep the knowledge imparted to students in the field relevant and adequate. The objective of this engineering course is to ensure graduates have a comprehensive engineering education combined with specialist knowledge of electronics and communication engineering. The students are provided with ample opportunities throughout the 4 year course to develop their theoretical, analytical, design and practical problem-solving aptitudes necessary towards engineering practice. Innovation, scientific rigour, ethical attitude and a sense of purpose for the benefit of society form the core values associated with the delivery of the programme.

The Department aims to achieve academic excellence by providing education such that graduates can assume key roles in engineering practice and applied research in industry, in the private sector and in public service. The programme has been designed to meet the competency standards prescribed by engineering bodies forming part of the Washington Accord for recognition of qualifications and international mobility of engineers. The first three years of the programme cover the fundamentals of electrical and electronic engineering. The final year allows students to focus in one of the proposed areas of specialisation in Communication Engineering.

2. Objectives

The objectives are to allow students to:

- Develop a sound mastery of the fundamentals of Electronic and Communication Engineering;
- Acquire skills in interpreting, simulating, modelling, designing, analysing and solving Electronic and Communication Engineering problems;
- Develop an understanding of the responsibilities of engineers as professionals particularly in terms of ethics and as contributors to the sustainable development objective;
- Develop a critical mind, independent learning ability, communication, teamwork, management and leadership skills essential in Electronic and Communication Engineering practice.

3. General Entry Requirements

As per General Entry Requirements for admission to the University of Mauritius for Undergraduate Degrees.

4. Programme Requirements

GCE ‘A’ Level Passes in Mathematics and Physics.
Pass at ‘O’ Level Chemistry.
5. Minimum Requirements for Degree Award

The award of the degree is subject to the student satisfying the following requirements:

- Successful completion of 145 UoM Credits (588 Notional Hours (NH) Credits) as per the programme structure;
- Satisfactory completion of industrial placements and workshop practice as specified in the programme;
- Satisfactory performance in each of the Exit Level Outcomes (ELOs) specified against modules in the module specification sheets.

The programme has been formulated to meet the competency standards prescribed by the Washington Accord. This means that graduates from this programme would in addition to satisfying the prescribed credits per knowledge area (basic sciences, mathematics, engineering sciences, and complementary studies) would also have demonstrated satisfactory performance in the following 11 Exit Level Outcomes:

ELO1: Problem Solving
ELO2: Application of scientific and engineering knowledge
ELO3: Engineering Design
ELO4: Investigations, experiments and data analysis
ELO5: Engineering methods, skills and tools, including Information Technology
ELO6: Professional and technical communication
ELO7: Sustainability and impact of Engineering activity
ELO8: Individual, team and multidisciplinary working
ELO9: Independent learning ability
ELO10: Engineering Professionalism
ELO 11: Engineering Management

6. Programme Duration

<table>
<thead>
<tr>
<th>Normal</th>
<th>Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Degree:</td>
<td>4 years</td>
</tr>
</tbody>
</table>

7. Classification of Awards

The award classification will be based on the CPA (x) at the end of the Programme of Studies as follows:

<table>
<thead>
<tr>
<th>CPA</th>
<th>CLASSIFICATION</th>
</tr>
</thead>
<tbody>
<tr>
<td>≥ 70</td>
<td>1st Class</td>
</tr>
<tr>
<td>60 ≤ x &lt; 70</td>
<td>2nd Class 1st Division</td>
</tr>
<tr>
<td>50 ≤ x &lt; 60</td>
<td>2nd Class 2nd Division</td>
</tr>
<tr>
<td>&lt; 50</td>
<td>No Award</td>
</tr>
</tbody>
</table>

Note: The general University Regulations pertaining to Exit Points would not be applicable to this programme.

8. Pre-Requisite Modules (PR)

A student will be allowed to follow module y of which module x is a pre-requisite (PR) provided he/she has satisfactorily completed module x with at least a pass grade.
9. Assessment and Pass Requirements

The assessment mode for each module will be based on one or a combination of the following:

- Examination
- Continuous assessment (class tests, assignments, practicals and oral presentations)
- Mini projects
- Practical and other reports
- Presentations
- Attendance to seminars

In order to pass a module a student must obtain an examination mark of at least 40% and a final mark of at least 50%.

**Calculation of the final mark:** The continuous assessment must account for no less than 30% and for no more than 50% of the final mark, with the exception of modules like design and research projects. Certain modules are assessed on the basis of 100% Continuous Assessment. The specific details and/or formula for the calculation of the final mark are given in the Module Specification Sheet (MSS) of each module.

Students have to retake both continuous assessment and exams in the failed module except in case of Resit Examinations; See provisions for Resit Examinations at Section 10. Students passing failed modules will score maximum marks of 50% in these modules but will have the failed marks not counted in the computation of the CPA.

If the student’s CPA is between 40 and 50, he/she fails the year. However, student will be eligible to repeat the year, will maintain credits and marks for individual modules where the mark scored is 50% or above. If the CPA is less than 40, the registration will be terminated.

**Rules in Cases of Unsatisfactory Performance of ELOs**

The ELOs and assessment criteria are specified against modules in the module specification sheets (MSS).

A student must comply with the subminimum requirements in subdivisions of certain modules. For such modules these specific requirements are given in the MSS of the module. These sub-minima include the achievement of ELOs that are assessed in the module. A sub minimum mark of 50% is required for all assessed elements (relevant questions in an assessment, project or assignment) in which the achievement of exit level outcomes are assessed (for the particular module). The following rules will apply in cases of unsatisfactory performance of ELOs.

(i) **ELOs assessed in the written examination**
A student failing the assessment of an ELO in written examination will be deemed to have failed the module. The student will have to retake the module next time it is offered. However, a Resit examination may be granted for the module only if a pass mark of at least 50% has been obtained; See the rules for Resit examinations at Section 10(iii).

(ii) **ELOs assessed in coursework, e.g., mini-project work**
A student not satisfying an ELO may be given an extension by the lecturer and the moderator prior to the written examination to amend and resubmit the coursework for pass mark of 50 % only. In case the student still fails to satisfy the ELO in the re-submission, he/she will be awarded Grade N in the module and will have to do a new coursework in the next academic year, provided he/she has scored a minimum of 50 % in the overall module mark.
In case a student fails the module, that is, scored less than 50% in the overall module mark, he/she will be awarded Grade F and has to retake the whole module the next time it is offered.

(iii) ELOs (other than ELO 6) assessed in the Final Year Project
If a candidate fails to obtain a pass mark of 50% for any ELO (other than ELO 6) in the Final Year Project, the Board of Examiners may consider one of the following:

- For a project/dissertation with possibility of amendments, award the student Grade N in the module and grant the student an extension period of up to 3 months to amend the work related to the ELO, and resubmit for pass mark of 50% in the ELO;
- For a project/dissertation with recommendations for a new submission, award the student Grade F in the module and student will have to undertake a new project in the following academic year.

(iv) ELO 6 assessed in the Design Project and/or Final Year Project
For a student failing to obtain the pass mark of 50% for ELO 6 in the Design Project and/or Final Year Project, the Board of Examiners may consider awarding the student Grade N and granting the student an extension period of up to 3 months to amend the components of the work related to this ELO, and resubmit the Design Project and/or Final Year Project for a pass mark of 50% in the ELO, provided that the student has scored a minimum of 50% in the overall module mark.

In case a student fails the module, that is, scored less than 50% in the overall module mark, he/she will be awarded Grade F and has to retake the Design Project and/or Final Year Project the next time it is offered.

(v) ELO 3 assessed in the Design Project
A student failing ELO 3 will be awarded Grade F in the design project and will have to retake the module the next time it is offered.

The detailed assessment mode for each module is given in the MSS.

10. Resit Examinations
If a student obtains a CPA of at least 50 but has not passed all the modules, a Resit examination may be granted for failed modules by the Board of Examiners provided that:

(i) A minimum of 40% has been obtained in continuous assessment.
(ii) A Final mark of at least 40% has been achieved in the failed modules which exclude assessment of ELOs;
(iii) A pass mark has been achieved but the required sub minimum for passing an Exit Level Outcome (ELO) has not been obtained.

Resit examinations do not apply to final year Project/Dissertation/Mini-Project Portfolio/Industrial Training and to modules assessed solely by continuous assessment.

11. Duration of examinations
16 NH credits modules shall have 3-hour examination papers. 12 NH credits and 8 NH credits modules shall have 2-hour examination papers.

12. Termination of Registration
Termination of registration will occur in the following circumstances:

- If the CPA is less than 25 at the end of Semester 1, Level 1.
- If the CPA is less than 40 at the end of an academic year.
- If the student fails to obtain credit in a module which he/she is repeating. This excludes Resit
13. Unless otherwise decided by Faculty Board, the following will apply for:

Progression from lower level to higher level

First Year to Second Year
A student should not have failed more than two modules to be able to register for Second Year modules. If any of the failed modules is a Pre-requisite(s) for a Second Year module, then the candidate cannot register for the PR-linked Second Year module until the Pre-requisite(s) is passed.

Second Year to Third Year
A student must have passed all prescribed First Year modules. In addition, the student should not have failed more than two modules of the prescribed second year modules to be able to register for Third Year modules. If any of the failed modules is a Pre-requisite(s) for a Third Year module, then the candidate cannot register for the PR-linked Third Year module until the pre-requisite is passed.

Third Year to Fourth Year
A student must have passed all prescribed second year modules. In addition, the student should not have failed more than two modules of the prescribed Third Year modules to be able to register for Fourth Year modules. If any of the failed modules is a pre-requisite for a Fourth Year module, then the candidate cannot register for the PR-linked Fourth Year module until the pre-requisite is passed.

Note: If a student is not proceeding to the next level, s/he is deemed to repeat the year, even if the CPA $\geq 50$.

14. Registration for Modules in a Higher Year of study for Repeating Students

If a student is repeating a year and the CPA is above 45, the student may be allowed to register for a maximum of two modules per semester from the higher year of study. The student will need to make a request to the Dean of Faculty. The student cannot register for a module of a higher year of study if a timetable clash occurs with a module of a previous year which has not yet been passed and which is prescribed for his or her field of study. Moreover, registration for modules is subject to pre-requisites being met.

15. Self-Development (SD)

This refers to directly supervised work in terms of hours/week. It includes practicals, tutorials, seminars, visits, mini-projects, oriented-discussion, coached group-work, presentations and other structured activities associated to enhancing the engineering application abilities and professional and personal attributes of the students. Such supervised work is included in the time-table.
# 16. BEng (Hons) Electronic and Communication Engineering Programme Structure

## Year 1- Semester 1

<table>
<thead>
<tr>
<th>Module Code</th>
<th>Module Name</th>
<th>Hours/Week (L + SD)</th>
<th>UoM Credits</th>
<th>Notional Hours</th>
<th>Credits</th>
<th>Pre-requisites</th>
</tr>
</thead>
<tbody>
<tr>
<td>ELEC 1107(1)</td>
<td>Physics for Engineers 1</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 1108(1)</td>
<td>Engineering Mathematics 1</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 1109(1)</td>
<td>Programming Techniques 1</td>
<td>2 + 2</td>
<td>3</td>
<td>12</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 1110(1)</td>
<td>Electro-techniques</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 1111(1)</td>
<td>Professional Communication for Electrical Engineers</td>
<td>2 + 2</td>
<td>3</td>
<td>12</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 1112(1)</td>
<td>Chemistry</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td></td>
<td>20</td>
<td>80</td>
<td></td>
</tr>
</tbody>
</table>

## Year 1- Semester 2

<table>
<thead>
<tr>
<th>Module Code</th>
<th>Module Name</th>
<th>Hours/Week (L + SD)</th>
<th>UoM Credits</th>
<th>Notional Hours</th>
<th>Credits</th>
<th>Pre-requisites</th>
</tr>
</thead>
<tbody>
<tr>
<td>CHE 1205(1)</td>
<td>Material Science</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 1208(1)</td>
<td>Engineering Mathematics 2</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 1209(1)</td>
<td>Statics</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 1201(1)</td>
<td>Physics for Engineers 2</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 1211(1)</td>
<td>Programming Techniques 2</td>
<td>2 + 2</td>
<td>3</td>
<td>12</td>
<td></td>
<td></td>
</tr>
<tr>
<td>SOCI 1207(1)</td>
<td>Contemporary Society &amp; Development</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 1220</td>
<td>Vacation Training - Workshop Practice</td>
<td></td>
<td></td>
<td></td>
<td>0</td>
<td></td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td></td>
<td>19</td>
<td>76</td>
<td></td>
</tr>
</tbody>
</table>

## Year 2 - Semester 1

<table>
<thead>
<tr>
<th>Module Code</th>
<th>Module Name</th>
<th>Hours/Week (L + SD)</th>
<th>UoM Credits</th>
<th>Notional Hours</th>
<th>Credits</th>
<th>Pre-requisites</th>
</tr>
</thead>
<tbody>
<tr>
<td>ELEC 2103(3)</td>
<td>Circuits &amp; Systems</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
<td>ELEC 1208(1), ELEC 1110(1)</td>
</tr>
<tr>
<td>ELEC 2109(3)</td>
<td>Power Systems 1</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
<td>ELEC 1110(1)</td>
</tr>
<tr>
<td>ELEC 2110(3)</td>
<td>Digital Electronics</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 2111(3)</td>
<td>Analog Electronics 1</td>
<td>2 + 2</td>
<td>3</td>
<td>12</td>
<td></td>
<td>ELEC 1110(1)</td>
</tr>
<tr>
<td>ELEC 2112(3)</td>
<td>Economics &amp; Accounting</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ELEC 2113(3)</td>
<td>Thermodynamics</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
<td>ELEC 1107(1)</td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td></td>
<td>19</td>
<td>76</td>
<td></td>
</tr>
</tbody>
</table>
## Year 2 - Semester 2

<table>
<thead>
<tr>
<th>Module Code</th>
<th>Module Name</th>
<th>Hours/Week (L + SD)</th>
<th>UoM Credits</th>
<th>Notional Hours Credits</th>
<th>Pre-requisites</th>
</tr>
</thead>
<tbody>
<tr>
<td>ELEC 2201(3)</td>
<td>Microprocessors &amp; Microcontrollers</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
</tr>
<tr>
<td>ELEC 2202(3)</td>
<td>Engineering Probability &amp; Statistics</td>
<td>3+2</td>
<td>4</td>
<td>16</td>
<td></td>
</tr>
<tr>
<td>ELEC 2203(3)</td>
<td>Instrumentation &amp; Measurement 1</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>ELEC 2204(3)</td>
<td>Communication Systems 1</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td>ELEC 1208(1)</td>
</tr>
<tr>
<td>ELEC 2205(3)</td>
<td>Electrical Machines</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td>ELEC 1110(1)</td>
</tr>
<tr>
<td>ELEC 2207(3)</td>
<td>Introduction to Engineering Design</td>
<td>Over two weeks during vacation</td>
<td>2</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td>20</td>
<td>80</td>
<td></td>
</tr>
</tbody>
</table>

## Year 3 - Semester 1

<table>
<thead>
<tr>
<th>Module Code</th>
<th>Module Name</th>
<th>Hours/Week (L + SD)</th>
<th>UoM Credits</th>
<th>Notional Hours Credits</th>
<th>Pre-requisites</th>
</tr>
</thead>
<tbody>
<tr>
<td>ELEC 3108(5)</td>
<td>Communications Systems Design</td>
<td>1 + 6</td>
<td>4</td>
<td>20</td>
<td>ELEC 2204(3), ELEC 2207(3)</td>
</tr>
<tr>
<td>ELEC 3102(5)</td>
<td>Control Systems 1</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td>ELEC 2103(3)</td>
</tr>
<tr>
<td>ELEC 3103(5)</td>
<td>Analog Electronics 2</td>
<td>2 + 2</td>
<td>3</td>
<td>12</td>
<td>ELEC 2111(3)</td>
</tr>
<tr>
<td>ELEC 3104(5)</td>
<td>Power Electronics 1</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td>ELEC 2103(3), ELEC 2205(3)</td>
</tr>
<tr>
<td>MECH 3107(5)</td>
<td>Project Management</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td>17</td>
<td>72</td>
<td></td>
</tr>
</tbody>
</table>

## Year 3 - Semester 2

<table>
<thead>
<tr>
<th>Module Code</th>
<th>Module Name</th>
<th>Hours/Week (L + SD)</th>
<th>UoM Credits</th>
<th>Notional Hours Credits</th>
<th>Pre-requisites</th>
</tr>
</thead>
<tbody>
<tr>
<td>ELEC 3202(5)</td>
<td>Engineering Mathematics</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
</tr>
<tr>
<td>ELEC 3206(5)</td>
<td>Digital Signal Processing 1</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>ELEC 3204(5)</td>
<td>Electronics Systems Design</td>
<td>1 + 6</td>
<td>4</td>
<td>20</td>
<td>ELEC 2110(3), ELEC 2207(3)</td>
</tr>
<tr>
<td>ELEC 3205(5)</td>
<td>Communication Systems 2</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td>ELEC 2204(3)</td>
</tr>
<tr>
<td>ELEC 3208(5)</td>
<td>Electromagnetics</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>CHE 3211(5)</td>
<td>Environmental Management</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>ELEC 3210</td>
<td>Industrial Placement</td>
<td>0</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td>18</td>
<td>76</td>
<td></td>
</tr>
</tbody>
</table>
### Year 4 - Semester 1

<table>
<thead>
<tr>
<th>Module Code</th>
<th>Module Name</th>
<th>Hours/Week (L + SD)</th>
<th>UoM Credits</th>
<th>Notional Hours Credits</th>
<th>Pre-requisites</th>
</tr>
</thead>
<tbody>
<tr>
<td>ELEC 4101(5)</td>
<td>Wireless and Mobile Communications</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td>ELEC 3205(5)</td>
</tr>
<tr>
<td>ELEC 4102(5)</td>
<td>Data Communications &amp; Networking</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td>ELEC 3205(5)</td>
</tr>
<tr>
<td>ELEC 4100Y(5)</td>
<td>Degree Project (first part)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ENGG 4102(5)</td>
<td>Sociology for Engineers</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Elective* (as per list below)</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>TOTAL</td>
<td></td>
<td></td>
<td>16</td>
<td>64</td>
<td></td>
</tr>
</tbody>
</table>

### Year 4 - Semester 2

<table>
<thead>
<tr>
<th>Module Code</th>
<th>Module Name</th>
<th>Hours/Week (L + SD)</th>
<th>UoM Credits</th>
<th>Notional Hours Credits</th>
<th>Pre-requisites</th>
</tr>
</thead>
<tbody>
<tr>
<td>ELEC 4100Y(5)</td>
<td>Degree Project (second part)</td>
<td></td>
<td>4</td>
<td>16</td>
<td></td>
</tr>
<tr>
<td>ELEC 4208 (5)</td>
<td>Optical Fibre Communications</td>
<td>3 + 2</td>
<td>4</td>
<td>16</td>
<td></td>
</tr>
<tr>
<td>MECH 4201(5)</td>
<td>Engineering Professionalism</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>ELEC 4212(5)</td>
<td>Design Project</td>
<td></td>
<td>6</td>
<td>24</td>
<td>ELEC 3108(5),</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>ELEC 3204(5)</td>
</tr>
<tr>
<td>TOTAL</td>
<td></td>
<td></td>
<td>16</td>
<td>64</td>
<td></td>
</tr>
</tbody>
</table>

*Choose any ONE of the following Elective modules:

<table>
<thead>
<tr>
<th>Module Code</th>
<th>Module Name</th>
<th>Hours/Week (L + SD)</th>
<th>UoM Credits</th>
<th>Notional Hours Credits</th>
<th>Pre-requisites</th>
</tr>
</thead>
<tbody>
<tr>
<td>ELEC 4111(5)</td>
<td>RF and Microwave Engineering</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>ELEC 4112(5)</td>
<td>Information Theory</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td>ELEC 3205(5)</td>
</tr>
<tr>
<td>ELEC 4113(5)</td>
<td>Data and Network Security</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td>ELEC 4102(5)</td>
</tr>
<tr>
<td>ELEC 4108(5)</td>
<td>Nanoelectronics</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td>ELEC 3103(5)</td>
</tr>
<tr>
<td>ELEC 4110(5)</td>
<td>Digital Signal Processing 2</td>
<td>1.5 + 1</td>
<td>2</td>
<td>8</td>
<td>ELEC 3206(5)</td>
</tr>
</tbody>
</table>

Total Notional hours credits = 588

Total UoM Credits = 145